

### PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re <u>PATENT APPLICATION</u> OF Inventor(s): <b>Naotoshi NISHIOKA</b> Appln. No.: 10   829,253		ATTENTION:	Office of Initial Patent Examination Filing Receipt Corrections
	erial No. ↑		
Title: SEMICONDUCTOR MEMORY DEVICE WITH CONFIGURABLE ON-CHIP DELAY CIRCUIT			
Date: June 13, 2004  Commissioner of Patents P.O. Box 1450  Alexandria, VA 22313-1450			
Sir: REQUEST FOR CORRECTED FILING RECEIPT			
<ol> <li>Attached is a copy of the official filing receipt from the PTO in the above application for which issuance of a corrected filing receipt is respectfully requested.</li> </ol>			
2. There is an error with respect to the following data which is			
	or in e	Last nameNishiok 2.	omitted Correct data of inventor "Nichioka" should read a
3. Title 3.			
4. Filing Date 4.			
5. Serial Number 5.			
6. Foreign/PCT Application Re 6.			
7. Other 7.			
<ol> <li>It is understood that the PTO no longer charges for correcting filing receipts even when the error is applicants' mistake, but if that is incorrect and a fee is due for this correction please charge our Deposit Account No. 16- 1805 under Order No. 051270-0309413.</li> </ol>			
	Pillsbury Winthrop LLP Intellectual Property Group		
725 South Figueroa Street, Suite 2800	By Atty: Roger R. W	/ise	Reg. No. 31204
Los Angeles, CA 90017-5406 Tel: (213) 488-7100 Atty/Sec: RRW/msg	Sig:	guR.W	Fax: (213) 629-1033 Tel: <b>(213) 488-7584</b>



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UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address COMMISSIONER FOR PATENTS PO. Box 1450 Alexandra, Vigoia 22313-1450

DRAWINGS

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04/22/2004

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ATTY.DOCKET NO 0309413/H8157US

CONFIRMATION NO. 7366

Roger R. Wise Pillsbury Winthrop LLP Suite 1200 725 South Figueroa Street

Los Angeles, CA 90017-5443

FILING RECEIPT

CC000000013087197

Date Mailed: 06/28/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon, If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filling Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Assignment For Published Patent Application

Yamaha Corporation, Hamamatsu-shi, JAPAN:

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2003-125362 04/30/2003

If Required, Foreign Filing License Granted: 06/26/2004

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Early Publication Request: No

RECEIVED PILLSBURY WINTHROP LLP/LA

JUL 0 1 2004

CL# 091270 MT# 030941 YAMAHA

DUE: DKT BY (1) WS4

Title

Semiconductor memory device with configurable on-chip delay circuit

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